RClamp1821Z
Ultra Small RailClamp®
1-Line ESD protection

PROTECTION PRODUCTS - Z-Pak™

Description

RailClamp® TVS arrays are ultra low capacitance ESD protection devices designed to protect high speed data interfaces. They are designed to replace 0201 size multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and other portable electronics. This device offers desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

RClamp®1821Z has a maximum capacitance of only 0.8pF. This allows it to be used on circuits operating in excess of 3GHz without signal attenuation.

RClamp1821Z is in a 2-pin SLP0603P2X3B package. It measures 0.6 x 0.3 mm with a nominal height of only 0.25mm. Leads are finished with lead-free NiAu. Each device will protect one line operating at 18 volts. It gives the designer the flexibility to protect single lines in applications where arrays are not practical. The combination of small size and high ESD surge capability makes them ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

Features

- High ESD withstand Voltage: **+/-15kV** (Contact), **+/-18kV** (Air) per **IEC 61000-4-2**
- Able to withstand over 1000 ESD strikes per IEC 61000-4-2 Level 4
- Ultra-small **0201 package**
- Protects one data or power line
- Low reverse current: <5nA typical (VR=18V)
- Working voltage: +/- 18V
- Low capacitance: 0.8pF Maximum
- Solid-state silicon-avalanche technology

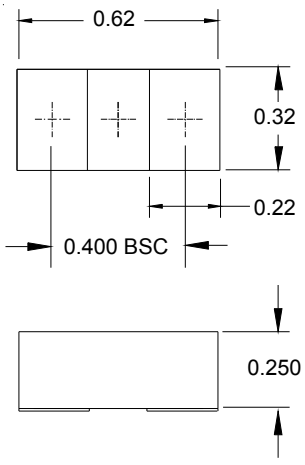
Mechanical Characteristics

- SLP0603P2X3B package
- Pb-Free, Halogen Free, RoHS/WEEE Compliant
- Nominal Dimensions: 0.6 x 0.3 x 0.25 mm
- Lead Finish: NiAu
- Marking : Marking code + dot matrix date code
- Packaging : Tape and Reel

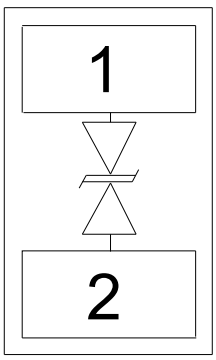
Applications

- Cellular Handsets & Accessories
- Near Field Communication (NFC) lines
- RF signal lines
- FM Antenna
- Digital Lines
- USB VBus

Nominal Dimensions



Schematic



SLP0603P2X3B (Bottom View)

Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp = 8/20µs)	P_{pk}	85	Watts
Maximum Peak Pulse Current (tp = 8/20µs)	I_{pp}	2.5	Amps
ESD per IEC 61000-4-2 (Air)[1] ESD per IEC 61000-4-2 (Contact)[1]	V_{ESD}	+/- 18 +/- 15	kV
Operating Temperature	T_J	-55 to +125	°C
Storage Temperature	T_{STG}	-55 to +150	°C

Electrical Characteristics (T=25°C)

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}	Pin 1 to 2 or 2 to 1			18	V
Reverse Breakdown Voltage	V_{BR}	I_t = 1mA Pin 1 to 2 or 2 to 1	20	22	24	V
Reverse Leakage Current	I_R	V_{RWM} = 18V, T=25°C Pin 1 to 2 or 2 to 1		5	50	nA
Clamping Voltage	V_C	I_{PP} = 2.5A, tp = 8/20µs Pin 1 to 2 or 2 to 1			34	V
ESD Clamping Voltage[2]	V_C	IPP = 4A, tlp = 0.2/100ns		30		V
ESD Clamping Voltage[2]	V_C	IPP = 16A, tlp = 0.2/100ns		48		V
Dynamic Resistance[2, 3]	R_D	tp = 100ns		1.5		Ohms
Junction Capacitance	C_j	V_R = 0V, f = 1MHz		0.63	0.8	pF

Notes

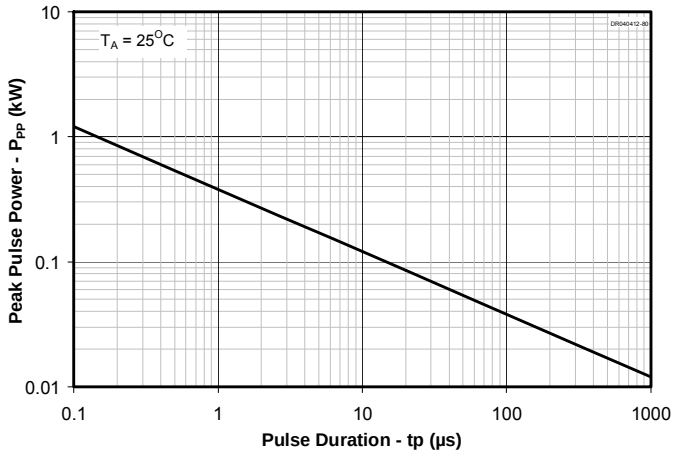
1)ESD gun return path connected to ESD ground reference plane.

2)Transmission Line Pulse Test (TLP) Settings: t_p = 100ns, t_r = 0.2ns, I_{TLP} and V_{TLP} averaging window: t_1 = 70ns to t_2 = 90ns.

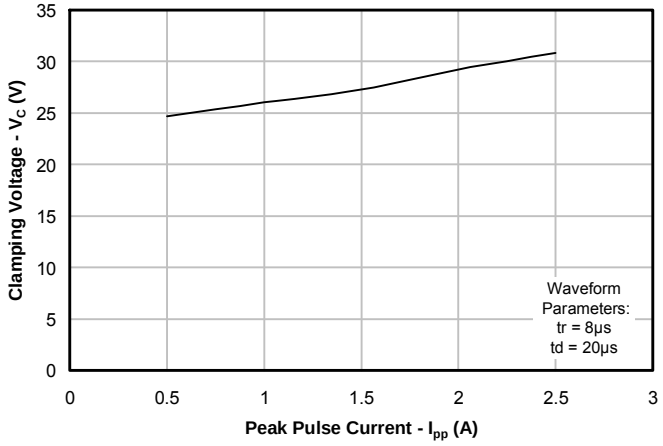
3) Dynamic resistance calculated from I_{TLP} = 4A to I_{TLP} = 16A

Typical Characteristics

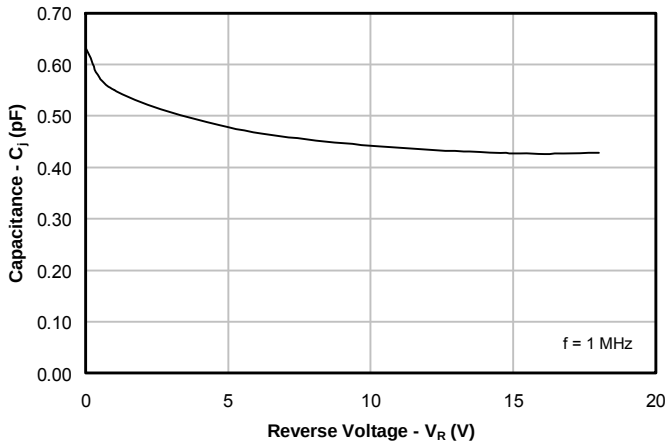
Non-Repetitive Peak Pulse Power vs. Pulse Time



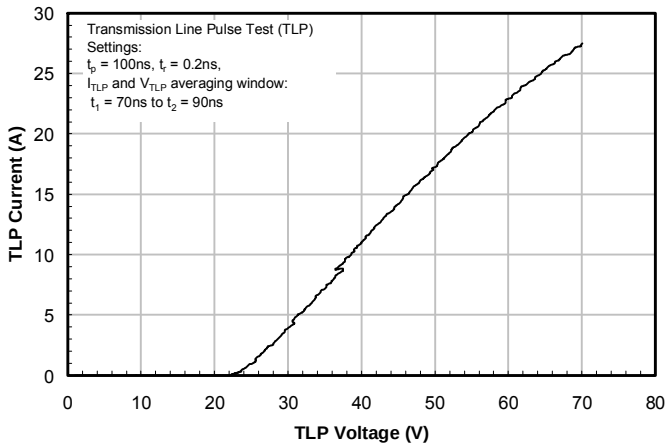
Clamping Voltage vs. Peak Pulse Current (tp=8/20us)



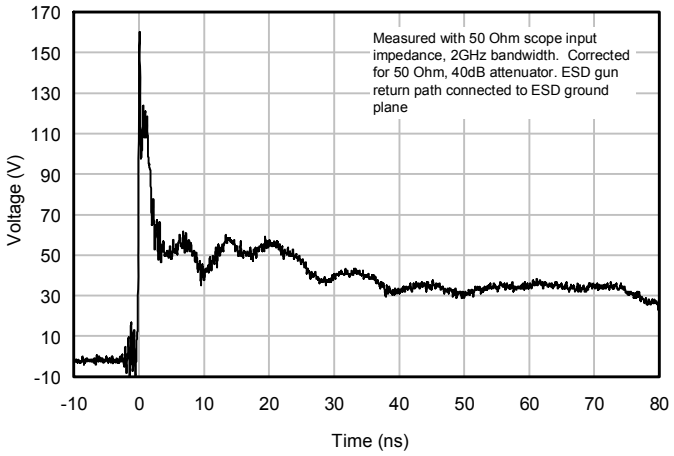
Junction Capacitance vs. Reverse Voltage



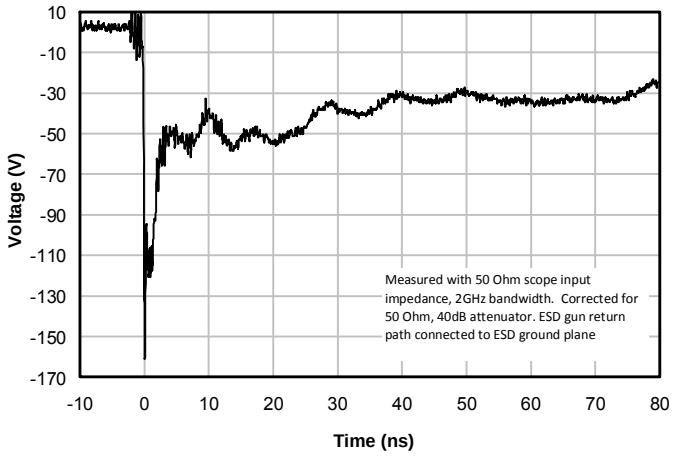
TLP Characteristic



ESD Clamping (+8kV Contact per IEC 61000-4-2)

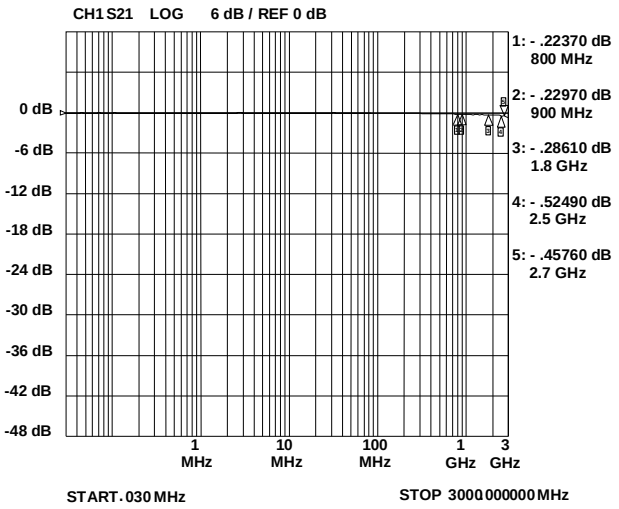


ESD Clamping (-8kV Contact per IEC 61000-4-2)



Typical Characteristics

Typical Insertion Loss S21

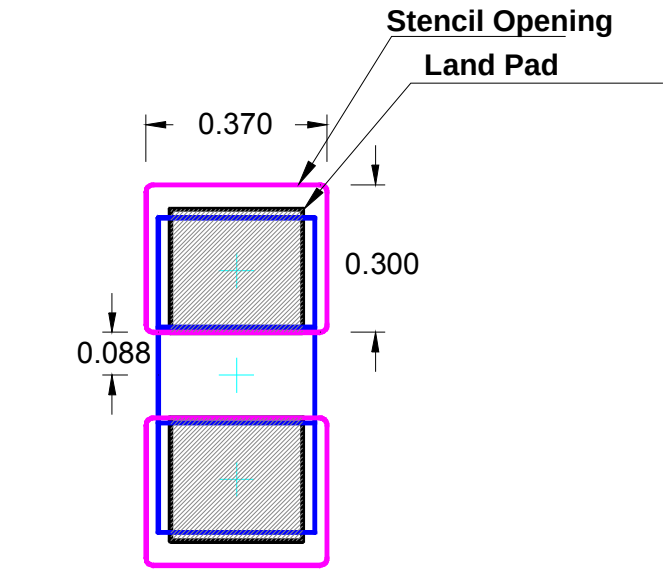


Applications Information

Assembly Guidelines

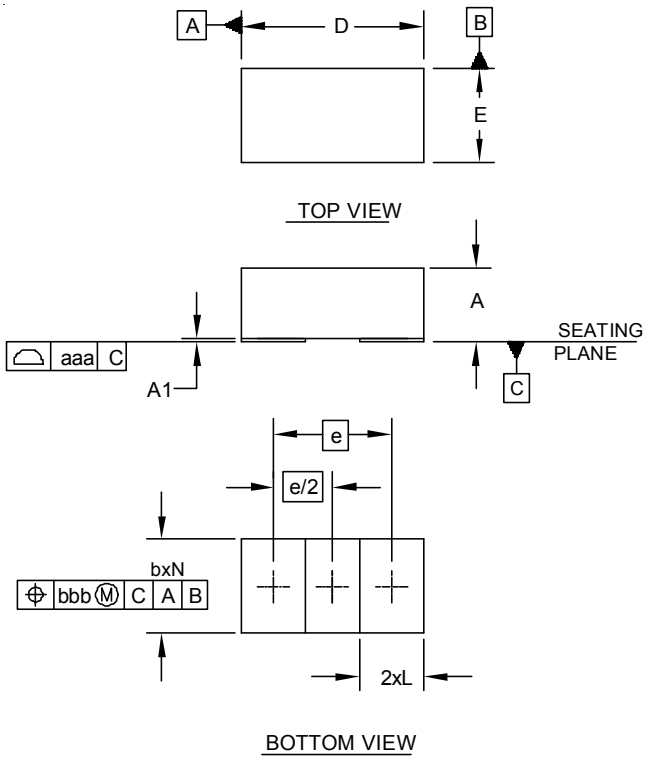
The small size of this device means that some care must be taken during the mounting process to insure reliable solder joint. The table below provides Semtech's recommended assembly guidelines for mounting this device. The figure at the right details Semtech's recommended aperture based on the below recommendations. Note that these are only recommendations and should serve only as a starting point for design since there are many factors that affect the assembly process. The exact manufacturing parameters will require some experimentation to get the desired solder application.

Assembly Parameter	Recommendation
Solder Stencil Design	Laser cut, Electro-polished
Aperture shape	Rectangular with rounded corners
Solder Stencil Thickness	0.100 mm (0.004")
Solder Paste Type	Type 4 size sphere or smaller
Solder Reflow Profile	Per JEDEC J-STD-020
PCB Solder Pad Design	Non-Solder mask defined
PCB Pad Finish	OSP OR NiAu



Recommended Stencil Design

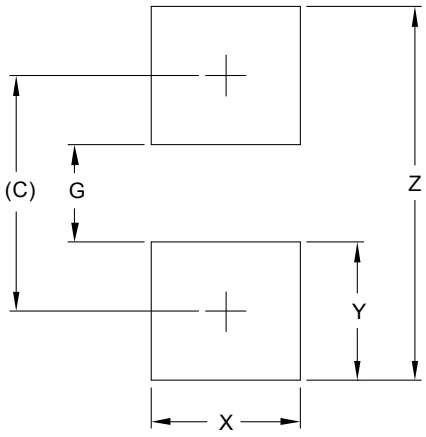
Outline Drawing - SLP0603P2X3B



DIM	DIMENSIONS MILLIMETERS		
	MIN	NOM	MAX
A	0.235	0.250	0.265
A1	0.000	0.010	0.050
b	0.300	0.320	0.340
D	0.605	0.620	0.675
E	0.285	0.320	0.355
e	0.40 BSC		
L	0.18	0.22	0.250
N	2		
aaa	0.08		
bbb	0.10		

NOTES:
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

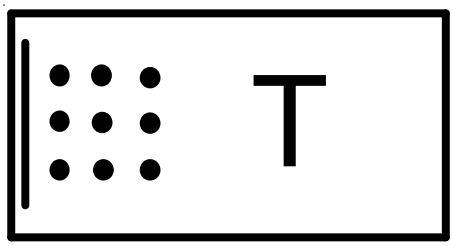
Land Pattern - SLP0603P2X3B



DIM	DIMENSIONS MILLIMETERS
C	(0.425)
G	0.175
X	0.270
Y	0.250
Z	0.675

NOTES:
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

Marking Code



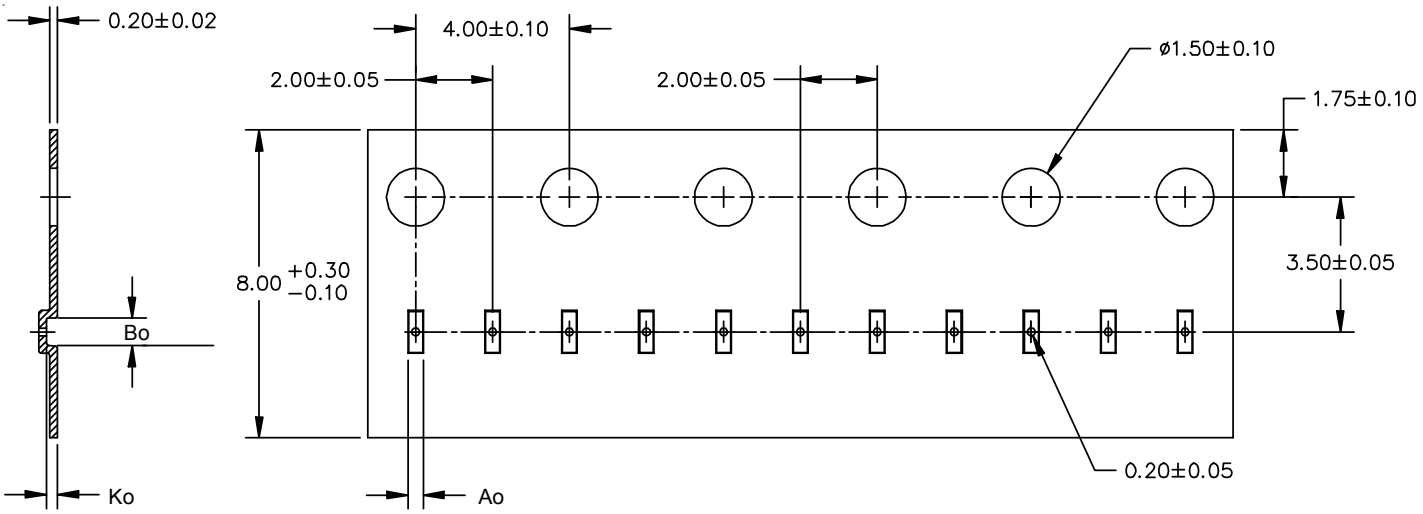
Notes:
1)Dots represent date code matrix and Pin 1 location

Ordering Information

Part Number	Qty per Reel	Reel Size
RClamp1821Z.TNT	10,000	7 Inch

Notes:
RailClamp and RClamp are trademarks of Semtech Corporation

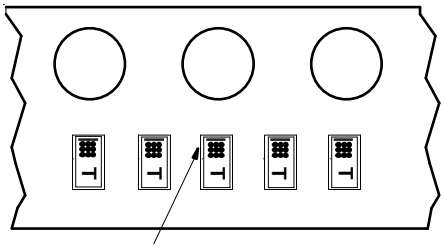
Carrier Tape Specification



A0	B0	K0
0.40 +/-0.05 mm	0.71 +/-0.05 mm	0.29 +/-0.05 mm

Note: All dimensions in mm unless otherwise specified

Device Orientation in Tape



PIN 1 Location
(Towards Sprocket Holes)

Contact Information

Semtech Corporation
Protection Products Division
200 Flynn Rd., Camarillo, CA 93012
Phone: (805)498-2111 FAX (805)498-3804

Mouser Electronics

Authorized Distributor

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